

CIP UNITED M-PANDA COMMUNITY ANNOUNCEMENT

In response to recent inquiries on MIPS open source, M-PANDA community would like to assure our partners in Greater China and beyond, that M-PANDA will continue to support the MIPS open source initiative and broaden the scope with additional programs. With the leadership of CIP United and other key partners, we have witnessed a resurgence of MIPS adoption in Great China market. A company with world-class R&D capability, CIP United aims to introduce a robust MIPS based CPU cores roadmap. M-PANDA has already established a platform and will continue to provide the open source version of the MIPS 32 and 64-bit Instruction Set Architectures (ISA), meanwhile, M-PANDA community will negotiate access for developers, partners and customers with model cores to inspire innovative approaches to system on chip (SoC) designs. Our intent is to enable members to easily sign up entry-level cores with minimal technology license fee, following the standard set by MIPS Open.

Since taking over MIPS business in our territory in the Spring of 2019, CIP United has been devoted to support the competitiveness of China IC design industry by offering mature and patent-protected CPU core IPs. Today, CIP United has built design teams in Shanghai and NingBo, and helped customers in Beijing, ShenZhen and many other cities to ensure SoC designers in China have the access to high quality, locally developed CPU cores and global resources. This effort has come into fruition as many top-tier IC design companies in different fields have chosen MIPS and CIP United as their technology partner.

Since its inception, M-PANDA has held seminars and coffee talks to enhance the knowledge level of MIPS CPU cores in China. Recently, we invited CPU design master Professor T. Riordan to give talks to selected members. Opening the MIPS ISA and expanding its surrounding ecosystem will turbo-charge advancement of the RISC architecture by leveraging the power of the community to modernize and deliver high-performance, multithreaded, top class solutions. The MIPS ISA represents 35-years of established and robust innovation in silicon-proven RISC core. To date, over 10B MIPS-based chips have shipped with a current run rate of over a billion a year. We will soon be announcing more open program to our partners throughout the world. Please contact M-PANDA for any inquiry.

M Panda Website: <https://www.m-panda.org.cn/>

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